

**Ahmad BSIESY**, professor in Physics/Microelectronics at University Grenoble Alpes-France where he obtained his PhD in 1991 on optoelectronic properties of porous silicon. He then spent 9 months as a post-doc at the electrical engineering department of Sherbrooke University (Canada). He was then appointed as an associate professor at the University of Grenoble where he pursued his research on porous silicon and developed a new “nano-photoelectrochemical” research activity by combining near field optics and electrolyte-semiconductor interface. In 2002 he was appointed professor at Spintec laboratory and took the lead of a team including magnetic material and semiconductor devices experts in order to develop research activity on silicon-based spintronic devices. In 2010, he joined the LTM laboratory where he develops a research activity on advanced integrated memory devices. Since 2008, he is director of the CIME Nanotech (Academic Centre for Microelectronics and Nanotechnology), the largest education center in microelectronics and technology in France and part of the French CNFM (Coordination Nationale de la Formation en Microélectronique et Nanotechnologies) education centers network. The CIME Nanotech hosts more than 1600 undergraduate or graduate students where they benefit from practical education facilities (clean rooms for semiconductor devices technology, nanophysics, biomedical engineering, smart devices for IoT,...). Prof. Ahmad Bsiesy has authored 87 scientific papers.